



INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
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COMPLETE IF KNOWN

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First Named Inventor	Charles E. Larson
Group Art Unit	2812
Examiner Name	(To Be Assigned)
Attorney Docket No.	108298502US2

Sheet 1 of 1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
<i>HL</i>		5,107,328		Kinsman	04/21/1992	
<i>HL</i>		5,138,434		Wood et al.	08/11/1992	
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<i>HL</i>		5,696,002		Frank et al.	12/09/1997	
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<i>HL</i>		6,693,342	B2	Larson et al.	02/17/2004	

CLASS
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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.	T
<i>HL</i>		Wolf and Tauber, Silicon Processing for the VLSI Era, Volume 1: Process Technology, pp. 27 and 30-31, 1986, Lattice Press, Sunset Beach, California.	

EXAMINER

DATE CONSIDERED

3 June 2004

*EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application(s).